



Substitute for Form 449A INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known	
		Application Number	09/981,620
		Filing Date	October 16, 2001
		First Named Inventor	Richard L. Coulson
		Art Unit	2189
		Examiner Name	K. Verbrugge
		Attorney Docket Number	42390P11456
Sheet	1	of	6

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number Number - Kind Code ² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
W		US-4,908,793	03-13-1990	Yamagata, et al.	
W		US-4,972,364	11-20-1990	Barrett, et al	
W		US-5,046,043	09-03-1991	Miller, et al	
W		US-5,133,060	07-21-1992	Weber et al	
W		US-5,269,019	12-07-1993	Peterson et al	
W		US-5,274,799	12-28-1993	Brant et al	
W		US-5,353,430	10-04-1994	Lautzenheiser	
W		US-5,444,651	08-22-1995	Yamamoto et al	
W		US-5,466,629	11-14-1995	Mihara et al	
W		US-5,526,482	06-11-1996	Stallmo et al	
W		US-5,542,066	07-30-1996	Mattson et al	
W		US-5,586,291	12-17-1996	Lasker et al	
W		US-5,604,881	02-18-1997	Thomas	
W		US-5,615,353	03-25-1997	Lautzenheiser	
W		US-5,701,516	12-23-1997	Cheng et al	
W		US-5,754,888	05-19-1998	Yang et al	
W		US-5,764,945	06-09-1998	Ballard	
W		US-5,918,244	06-29-1999	Percival	
W		US-6,025,618	02-15-2000	Chen	
W		US-6,052,789	04-18-2000	Lin	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.†	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Country Code - Number - Kind Code (if known)				
W		EP 0 210 384	02-24-1987	HEWLETT PACKARD		
W		GB 2 210 480	06-07-1989	Sun Microsystems		
W		WO 93/21579	10-28-1993	Storage Technology		
W		JP 6-236241	08-23-1994	Sharp		
W		WO 02/01364 A2	01-03-2002	Intel		
W		WO 02/01364 A3	01-03-2002	Intel		
W		WO 02/01365 A2	01-03-2002	Intel		
W		WO 02/01365 A3	01-03-2002	Intel		
W		WO 03/003202 A1	01-09-2003	Intel		
W		WO 03/003217 A2	01-09-2003	Intel		

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		Number - Kind Code ² (if known)			
W		US-6055180	04-25-2000	Gudesen et al	
W		US-6064615	05-16-2000	Gudesen	
W		US-6081883	06-27-2000	Popelka et al	
W		US-6101574	08-08-2000	Kumasawa et al	
W		US-6122711	09-19-2000	Mackenthun et al	
W		US-6295577	09-25-2001	Anderson et al	
W		US-6370614	04-09-2002	Teoman et al	
W		US-6438647	08-20-2002	Nielson et al	
W		US-6463509	10-08-2002	Teoman et al	
W		US-6498744	12-24-2002	Gudesen et al	
W		US-6539456	03-25-2003	Stewart	
W		US-6564286	05-13-2003	Decosta	
W		US-6662267	12-09-2003	Stewart	
W		US-6670659	12-30-2003	Gudesen et al	
W		US-6725342	04-20-2004	Coulson	
W		US-6785767	08-31-2004	Coulson	
W		US-6839812	01-04-2005	Royer, Jr. et al	
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FOREIGN PATENT DOCUMENTS

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		Examiner Name	K. Verbrugge
		Attorney Docket Number	42390P11456
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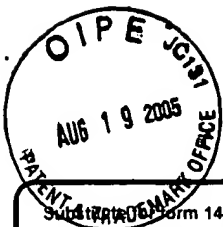
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
KV		Auciello, Orlando, et al., "The Physics of Ferroelectric Memories", Physics Today, pp. 22-27 (Jul. 1998).	
KV		Date, M., et al., "Opto-Ferroelectric Memories Using Vinylidene Fluoride Copolymers", IEEE, pp. 298-302 (1988).	
KV		Hodges, David A., et al., "Analysis and Design of Digital Integrated Circuits", McGraw-Hill, Inc., pp. 388-389 (1983).	
KV		Derwent Abstract, Derwent Accession No. 2000-348269, Patent Publication No. RD 431135 A, 3 pages (March 10, 2000).	
KV		IBM Technical Disclosure Bulletin, "Non-Volatile Cache Storage Allocation Algorithm", Vol. 38, No. 12, pp. 39-41 (Dec. 1995).	
KV		Lang, Sidney B., et al., "Pyroelectric Applications of Ferroelectric Polymers", IEEE, pp. 251-255 (1988).	
KV		Merz, W. J., et al., "Ferroelectric Storage Devices", Bell Laboratories Record, pp. 335-337, 339-342 (Sep. 1955).	

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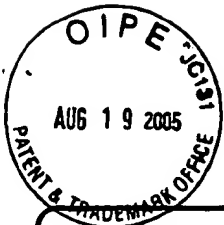
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
KW		Moazzami, Reza, et al., "A Ferroelectric DRAM Cell for High-Density NVRAM's", IEEE Electron Device Letters, Vol. 11, No. 10, pp. 454-456 (Oct. 1990).	
KW		Pulvari, C.F., "Ferroelectrics and their Memory Applications", IRE Transactions - Component Parts, pp. 3-11 (Mar. 1956).	
KW		Ramtron, FM1608 Preliminary Datasheet, Ramtron International Corporation, Colorado Springs, Colorado, pp. 1-12 (Dec. 7, 1999).	
KW		Ramtron, FM24C64 Preliminary Datasheet, Ramtron International Corporation, Colorado Springs, Colorado, pp. 1-13 (Dec. 7, 1999).	
KW		Ramtron, FRAM Technology, Ramtron International Corporation, Colorado Springs, Colorado, 2 pages (Jan. 1994).	
KW		Ramtron, "Replacing a Dallas Semiconductor DS1225 with FRAM Memory", Application Note, Ramtron International Corporation, Colorado Springs, Colorado, pp. 1-2 (Feb. 1994).	
KW		Robertson, Jack, "Hyundai Enters Japanese-dominated FeRAM Market", Silicon Strategies, A Service of Semiconductor Business News, CMP Media Inc., www.siliconstrategies.com (Sep. 3, 1998).	

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rw		U.S. Patent Application No. 09/602,008, entitled "In-Line Cache", by Richard Coulson, filed June 23, 2000.	
rw		U.S. Patent Application No. 09/602,009, entitled "Non-Volatile Cache Expansion Board", by Richard Coulson, filed June 23, 2000.	
rw		U.S. Patent Application No. 09/602,010, entitled "Non-Volatile Cache", by Richard Coulson, filed June 23, 2000.	
rw		U.S. Patent Application No. 09/602,011, entitled "Non-Volatile Cache Integrated with Mass Storage Device", by Richard Coulson, filed June 23, 2000.	
rw		White, Ron, "How Computers Work", Ziff-Davis Press, pp. 71-73 (1993).	

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